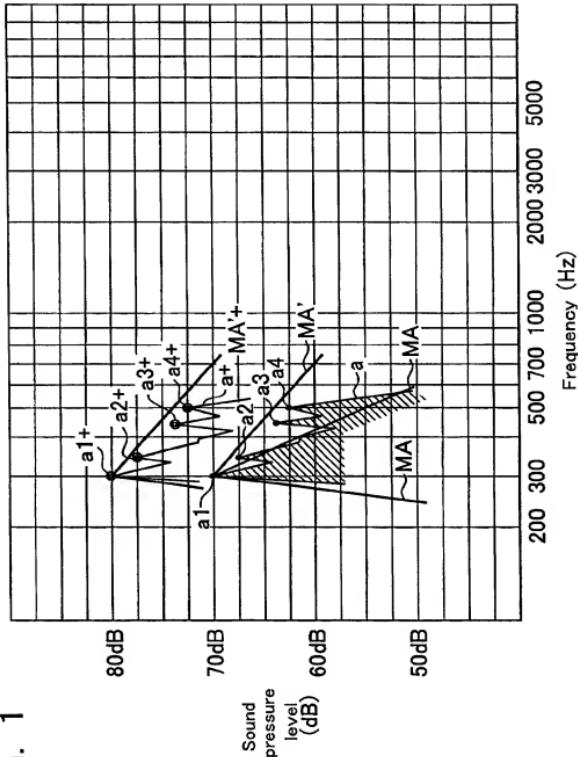


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FIG. 1



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FIG. 2

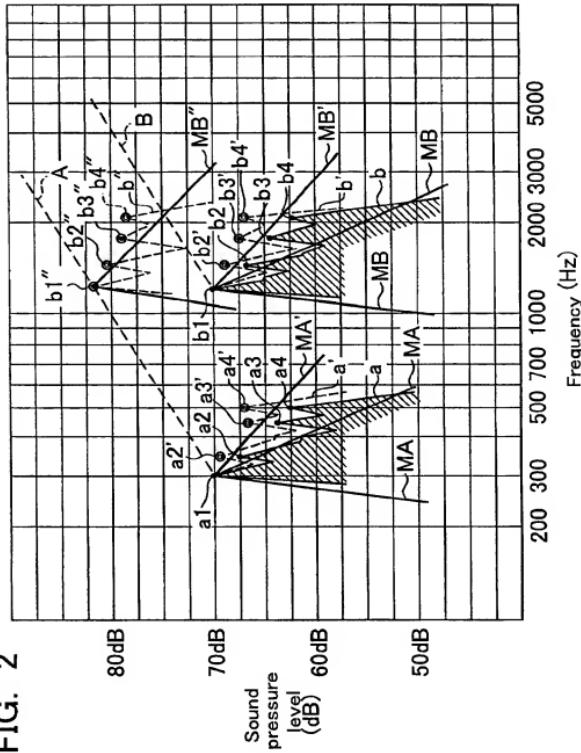
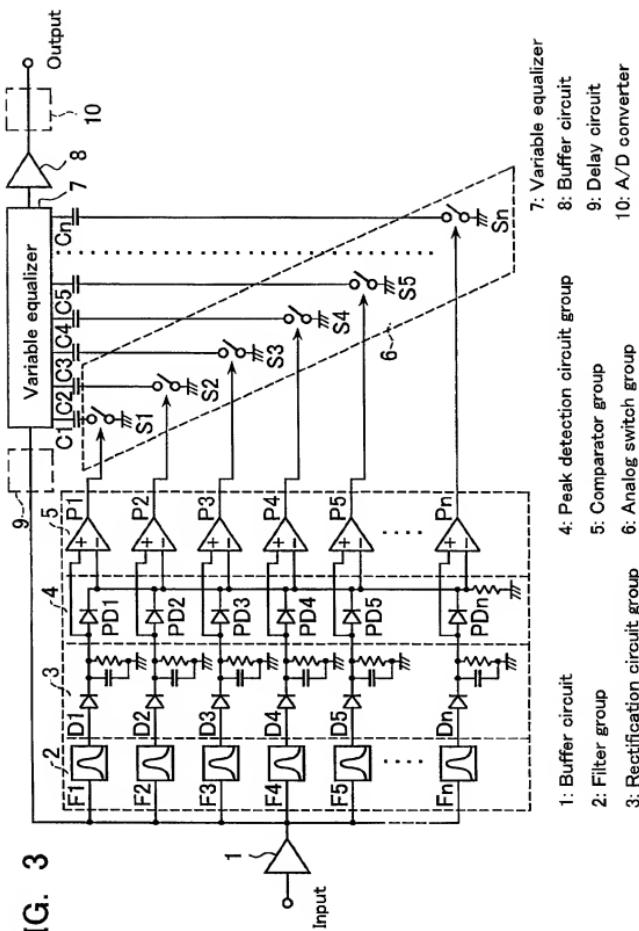


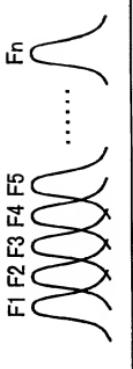
FIG. 3



1: Buffer circuit
2: Filter group
3: Rectification circuit group
4: Peak detection circuit group
5: Comparator group
6: Analog switch group

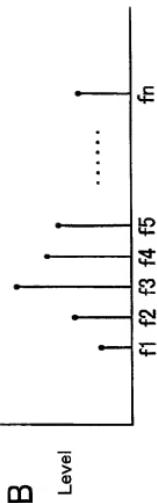
7: Variable equalizer
8: Buffer circuit
9: Delay circuit
10: A/D converter

FIG. 4A



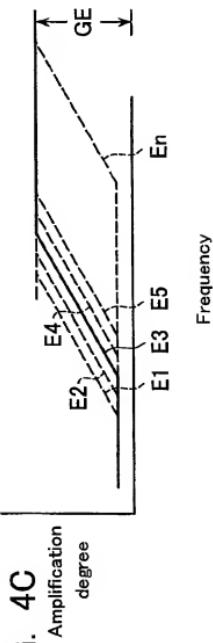
Characteristics of filters for
analyzing frequencies

FIG. 4B



Output levels of filters

FIG. 4C



Equalizer characteristic
changing according to output
level of filter

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FIG. 5

